



Attorney Docket SEL 291

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of)
Yamazaki et al.)
Serial No.: 09/993,993)
Filed: November 14, 2001)
For: Semiconductor Device And Method)
Of Manufacturing The Same)
Art Unit: 2826)
Examiner: M. Tran)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Shannon Wallace

Name of applicant, assignee, or Registered Rep.

Shannon Wallace 4/2/04

Signature

Date

RESPONSE (B)

Sir:

Applicants have the following response to the Office Action dated December 2, 2003, a one month extension of time being submitted herewith.

REMARKS

Applicants appreciate the Examiner's allowance of Claims 16 and 17 in the above-identified application. Applicants will now address the Examiner's remaining rejection in the Office Action.

Claim Rejections – 35 USC §103

In the Office Action, the Examiner rejects Claims 1-7, 11, 12, 15, 18 and 19 under 35 USC §103(a) as being obvious over Suzawa et al. This rejection is respectfully traversed.